

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|-------|---|---|------------------|---------|------------------|
| S1 | 6633 | (graphical and circuit and bus and executable) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/02 10:07 |
| S2 | 279 | S1 and "703".clas. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/06 09:36 |
| S3 | 84 | S2 and @pd<="20021001" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/02 10:22 |
| S4 | 1 | ((single adj line) same (grouping and (signal line conductor))) and (\$spice) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 10:28 |
| S5 | 47 | ((single adj line) same (group\$4 and (signal line conductor))) and (\$spice) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 10:29 |
| S6 | 13 | 703/15.ccls. and (bus adj signal) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 13:31 |
| S7 | 14769 | (bus adj3 operation) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 13:31 |
| S8 | 89 | S7 and (circuit adj simulat\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 13:31 |
| S9 | 60 | S8 and @pd<="20031001" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/02 13:32 |

EAST Search History

| | | | | | | |
|-----|-----|--|---|----|-----|------------------|
| S10 | 25 | (circuit adj simulation) and (bus same graphical) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 10:18 |
| S11 | 11 | 703/15.ccls. and (bus same graphical) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 10:21 |
| S12 | 24 | S10 not S11 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 21:42 |
| S13 | 4 | rostoker.in. and (bus adj signal adj line) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 14:16 |
| S14 | 252 | (703/14.CCLS. 703/15.ccls.) and (bus same simulat\$5) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 21:59 |
| S15 | 134 | S14 and @pd<="20031001" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 21:43 |
| S16 | 12 | (703/14.CCLS. 703/15.ccls.) and "715".clas. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 22:13 |
| S17 | 2 | "5881267".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 22:19 |
| S18 | 31 | virtual adj (mux multiplex\$3) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/05 22:21 |
| S19 | 473 | (gui (graphical adj interface)) and (group\$4 adj3 signal) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/05 22:21 |

EAST Search History

| | | | | | | |
|-----|------|---|---|----|-----|------------------|
| S20 | 27 | S19 and "703".clas. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/05 22:21 |
| S21 | 1569 | (netlist schematic) same (ide gui (graphical adj interface)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/06 09:36 |
| S22 | 9379 | (netlist schematic block core) same (ide gui (graphical adj interface)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/06 09:44 |
| S23 | 1321 | S22 and "715".clas. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/06 09:38 |
| S24 | 21 | S23 and "703".clas. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/06 09:39 |
| S25 | 222 | S22 and (bus adj signal) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/06 09:44 |
| S26 | 20 | S25 and ("703".clas. "716".clas. "715".clas.) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/06 09:44 |
| S27 | 0 | (network and design and tool and graphical).ti. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/06 12:05 |
| S28 | 9 | (network and design and tool).ti. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/06 12:05 |

[Google](#)craigs.list.jason@gmail.com | [Search History](#) | [My Account](#) | [Sign out](#)[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

xilinx manual

[Advanced Search](#)
[Preferences](#)**Web**Personalized Results 1 - 10 of about 345,000 for xilinx manual. (0.28 seconds)**Xilinx Software Manuals**

Xilinx : Support : Library ... View and search the entire **manual** collection with your web browser. support.xilinx.com feedback ... toolbox.xilinx.com/docsan/3_1/ - 20k - [Cached](#) - [Similar pages](#)

2.1i Software Manuals - support.xilinx.com

2.1i Software Manual PDF files. ... Xilinx/Concept-HDL Interface Guide (Alliance Software Only), docchdl.pdf.
Constraints Editor Guide, cst_edit.pdf ...
toolbox.xilinx.com/docsan/2_1/ - 27k - [Cached](#) - [Similar pages](#)

[PDF] Verilog HDL Reference Manual

File Format: PDF/Adobe Acrobat

This **manual** describes the Verilog portion of Synopsys FPGA Compiler II / FPGA Express ... FPGA Compiler II / FPGA Express Getting Started **Manual** ... www.cse.ucsc.edu/classes/cmpe225/Fall01/synver.pdf - [Similar pages](#)

vlsi-design: manual - xilinx

_xilinx/\$(PROJmodule).bit which is used to configure **Xilinx** FPGAs. Refer to the **manual** of the FPGA board you are using for information how this file is ... www.iaik.tugraz.at/teaching/05_vlsi-design/manual/xilinx/index.php - 18k - [Cached](#) - [Similar pages](#)

Xilinx Tools Tutorial (6.111 labkit)

The libraries guide in the **Xilinx** documentation provides an complete ... and an entire section of the online **manual** is dedicated to explaining them. ... web.mit.edu/6.111/www/s2004/NEWKIT/ise.shtml - 14k - [Cached](#) - [Similar pages](#)

[PDF] Xilinx Virtex-II Pro and Virtex-II Pro X.FPGA User Guide

File Format: PDF/Adobe Acrobat

The iMPACT Software **Manual** is included on the software manuals web page. . In-System Programming details are contained in **Xilinx** Application Note XAPP058. ... www.xilinx.com/bvdocs/userguides/ug012.pdf - [Similar pages](#)

[PDF] RocketIO™ Transceiver User Guide

File Format: PDF/Adobe Acrobat

Use of a **Xilinx** product in such applications without the written consent of the appropriate **Xilinx** officer is prohibited. The contents of this **manual** are ... www.xilinx.com/bvdocs/userguides/ug024.pdf - [Similar pages](#)

Document Services

Properties for VerilogHDL **Xilinx** **Manual** ... FPGA Compiler II / FPGA Express Verilog HDL Reference **Manual** Version 1999.05, May 1999 Comments? ... phys-ds.physics.lsa.umich.edu/docushare/dsweb/Services/Document-1178 - 37k -

Sponsored Links**Xilinx Distributor**

Xilinx parts in stock. Order Online Same Day Shipping , New and EOL www.sierraic.com

[Cached](#) - [Similar pages](#)Warthman Associates, Technical Writers - Xilinx Virtex-II Pro ...

This Xilinx manual describes a product line of reconfigurable logic with ... Xilinx

PPC405 User Manual for the Virtex-II Pro™ Platform FPGA Developer's Kit ...

www.warthman.com/projects-xilinx-VII-UM.htm - 9k - [Cached](#) - [Similar pages](#)

Warthman Associates, Technical Writers - Xilinx Virtex-II Pro ...

This Xilinx manual describes a product line of reconfigurable logic with ... Xilinx

PPC405 Processor Block Manual for the Virtex-II Pro™ Platform FPGA ...

www.warthman.com/projects-xilinx-VII-PBM.htm - 9k - [Cached](#) - [Similar pages](#)

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

Try [Google Desktop](#): search your computer as easily as you search the web.

[xilinx manual](#)[Search](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2007 Google